IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: MOSTAFAZADEH

Attorney Docket No.: NSC1P194/P04836

Application No.: 09/698,736

Examiner: THAI, L.C.

Filed: October 26, 2000

Group: 2811

Title: CHIP SCALE PIN ARRAY

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail to: Commissioner for Patents, Washington, DC/2023 I on March/20

Signed:

Sandra Halliwell



INFORMATION DISCLOSURE STATEMENT BEFORE FINAL ACTION OR NOTICE OF ALLOWANCE (37 CFR §§ 1.56 AND 1.97(c))

Commissioner for Patents Washington, DC 20231

Dear Sir:

The references listed in the attached PTO Form 1449, a copy of which is attached, may be material to examination of the above-identified patent application. Applicants submit this reference in compliance with their duty of disclosure pursuant to 37 CFR §§ 1.56 and 1.97. The Examiner is requested to make this citation of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application do not exist, or that this reference indeed constitutes prior art.

This Information Disclosure Statement is being filed after the mailing date of the first Office Action on the merits, but it is believed before the mailing date of either: (i) a final action under § 1.113 or (ii) a notice of allowance under § 1.311, whichever occurs first.

Enclosed is our Check No. 14501 for \$180.00 in payment of the Information Disclosure Statement Fee. If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. NSC1P194).

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Respectfully submitted,

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